| FACULTY: | Faculty of Electronics and Computer Science |
| --- | --- |
| FIELD OF STUDY: | Electronics and Telecommunications |
| ERASMUS COORDINATOR OF THE FACULTY: | Marcin Walczak, PhD (Electronics)  Robert Wirski, PhD (Computer Science) |
| E-MAIL ADDRESS OF THE COORDINATOR: | marcin.walczak@tu.koszalin.pl  robert.wirski@tu.koszalin.pl |
| COURSE TITLE: | Laboratory of Computer Architecture |
| LECTURER’S NAME: | Dariusz Gretkowski, PhD |
| E-MAIL ADDRESS OF THE LECTURER: | dariusz.gretkowski@tu.koszalin.pl |
| ECTS POINTS FOR THE COURSE: | 2 |
| COURSE CODE (USOS): | 0711>0400-AKiSO-lab |
| ACADEMIC YEAR: | 2022/2023 |
| SEMESTER:  (W – winter, S – summer) | W |
| HOURS IN SEMESTER: | 30 |
| LEVEL OF THE COURSE:  (1st cycle, 2nd cycle, 3rd cycle) | 1st cycle |
| TEACHING METHOD:  (lecture, laboratory, group tutorials, seminar, other-what type?) | Laboratory – 30h |
| LANGUAGE OF INSTRUCTION: | English |
| ASSESSMENT METHOD:  (written exam, oral exam, class test, written reports, project work, presentation, continuous assessment, other – what type?) | Continuous assessment |
| COURSE CONTENT: | Acquainting with "Architex" computer systems emulation program.  Realization of arithmetic operations - sum, difference and shift of 32-bit numbers in the "Architex" environment.  Realization of the operation of multiplication of 16-bit signed numbers in the "Architex" environment.  Microassembler instructions: loops, macros and subroutines.  The implementation of the instructions with one argument - Addressing the direct and indirect operands.  The implementation of the two-argument instructions - Direct and indirect registry's addressing.  Binary instruction implementation - autoincrementation and autodecrementation.  Execution of jump instructions.  System tools monitoring the operating system. |
| ADDITIONAL INFORMATION: |  |

………………………………………………………………..

/sporządził, data/

\*kurs dostępny wyłącznie w języku angielskim