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| FACULTY: | Faculty of Electronics and Computer Science |
| FIELD OF STUDY: | Computer Science |
| ERASMUS COORDINATOR OF THE FACULTY: | Marcin Walczak, PhD |
| E-MAIL ADDRESS OF THE COORDINATOR: | marcin.walczak@tu.koszalin.pl |
| COURSE TITLE: | Microprocessor Technology |
| LECTURER’S NAME: | Paweł Poczekajło, PhD |
| E-MAIL ADDRESS OF THE LECTURER: | pawel.poczekajlo@tu.koszalin.pl |
| ECTS POINTS FOR THE COURSE: | 3 |
| COURSE CODE (USOS): | 0711>0400-TM2 |
| ACADEMIC YEAR: | 2023/2024 |
| SEMESTER:  (W – winter, S – summer) | W |
| HOURS IN SEMESTER: | 30 |
| LEVEL OF THE COURSE:  (1st cycle, 2nd cycle, 3rd cycle) | 1st cycle |
| TEACHING METHOD:  (lecture, laboratory, group tutorials, seminar, other-what type?) | Lecture – 30h |
| LANGUAGE OF INSTRUCTION: | English, Polish, (separate group with English depends from number of the incoming students) |
| ASSESSMENT METOD:  (written exam, oral exam, class test, written reports, project work, presentation, continuous assessment, other – what type?) | written reports or presentation |
| COURSE CONTENT: | Microcontrollers and Embedded Processors,  Introduction to Microcontrollers (Motorola and AVR Family), Introduction to ATMega8 / ATMega16 / ATMega32 features, Introduction to M6800 / 68HC05 features, General purpose registers of AVR Controller, AVR Data Registers, Using of IO Ports, Parameters of Microcontrollers and they internal peripherals, Software and programing languages. |
| ADDITIONAL INFORMATION: | Requirements: hexadecimal and binary format, architecture of processors/controllers. |

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\*kurs dostępny wyłącznie w języku angielskim